



PATENT

IN THE UNITED STATES PATENT OFFICE

Applicants: Reno L. Sanchez et al.  
Assignee: Xilinx, Inc.  
Title: Method and System for Inserting Probe Points in  
FPGA-Based System-On-Chip  
Serial No.: 10/082,517 File Date: 02-22-02  
Examiner: Magid Y. Dimyan Art Unit: 2825  
Docket No.: X-998 US Conf. No.: 5981  
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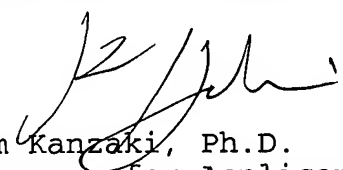
Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the three (3) references listed in the attached Substitute for Form 1449A/PTO. A copy of each is enclosed herein.

The enclosed reference(s) is/are being cited after the receipt of a first office action but prior to any final action.

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Respectfully submitted,

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*I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, on January 6, 2004.*

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**Complete if Known**

Applicati n / C nf. N .	10/082,517	/	5981
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(use as many sheets as necessary)

<b>Filing Date</b>	February 22, 2002
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<b>First Named Inventor</b>	Reno L. Sanchez
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Art Unit	2825
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Examiner Name	Magid Y. Dimyan
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Attorney Docket Number

Sheet	1	of	1
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Date Considered

<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See Kinds of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

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